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TRANSMITTAL FORM

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Total Number of Pages in This Submission | 13

10/614,905 **Application Number** July 7, 2003 Filing Date First Named Inventor Sergio Carmelo Art Unit 2836 **Examiner Name** Unassigned Attorney Docket Number CISCO-6920

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ENCLOSURES (check all that apply)				
Fee Transmittal F	Form	Drawing(s)		After Allowance Communication to Group
Fee Attached	l	Licensing-related F	apers	Appeal Communication to Board of Appeals and Interferences
Amendment / Rep	ply	Petition		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
After Final		Petition to Convert to a Provisional Application		Proprietary Information
Affidavits/dec	daration(s)	Power of Attorney, Revocation Change of Correspondence Address Status Letter		Status Letter
Extension of Time	e Request	Terminal Disclaime	ers (3)	Other Enclosure(s) (please identify below):
Express Abandonment Request		Request for Refund		Correction to Filing Receipt (2 pgs); copy of FR dated 10/3/2003 (2 pgs); copy of front page of
☐ Information Disclosure Statement				spec filed 7/7/2003 (1 pg); Patent Practitioners List (2 pgs); and Copy of Original Dec filed on 7/7/2003 (5 pgs).
Certified Copy of Priority Document(s)		Remarks		
Response to Miss Incomplete Applic				
Response to Missing Parts under 37 CFR 1.52 or 1.53				
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT				
Firm <i>or</i> Individual name	David B. Ritchie, Reg Thelen Reid & Priest			
Signature	1			
Date	11-4-2	004		·
CERTIFICATE OF TRANSMISSION/MAILING				

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Docket No.: CISCO-6920

E UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT

Sergio Camerlo et al.

SERIAL NO.:

10/614,905

FILING DATE:

July 7, 2003

TITLE:

Delivering High-Current Power and Ground Voltages Using Top Side of Chip

Package Substrate

EXAMINER:

Unassigned

ART UNIT:

2836

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

REQUEST TO CORRECT FILING RECEIPT

It is respectfully submitted that the Filing Receipt for the above-identified patent application has an error.

The title is incorrect; the correct title is "Delivering High-Current Power and Ground Voltages Using Top Side of Chip Package Substrate" and not "Delivering High-Current Power adn Ground Voltages Using Top Side of Chip Package Substrate" as indicated on the Filing Receipt.

Please amend the Filing Receipt and appropriate records to reflect the correct title, in the referenced patent application. A copy of the Filing Receipt is enclosed. In addition, also enclosed is a copy of the first page of the specification for the above referenced patent application. Please note that it correctly lists the title.

Docket No.: CISCO-6920

The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 50-1698.

Dated: November 4, 2004

David B. Ritchie Reg. No. 31,562

Respectfully submitted, THELEN READ & PRIEST LLP

THELEN REID & PRIEST LLP P.O. Box 640640

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UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Viginia 22313-1450

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FILING OR 371 ART UNIT APPL NO. **FIL FEE REC'D** ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS (c) DATE 2836 1170 **CISCO-6920** 34 5 10/614,905 07/07/2003

CONFIRMATION NO. 7936

David B. Ritchie Thelen Reid & Priest LLP P.O. Box 640640 San Jose, CA 95164-0640



FILING RECEIPT *OC000000010975257*

Date Mailed: 10/03/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Sergio Camerlo, Cupertino, CA; Yida Zou, San Jose, CA; Luca Cafiero, Palo Alto, CA; Gary L. Myers, San Ramon, CA; Bobby Parizi, San Jose, CA; Hsing-Sheng Liang, San Jose, CA;

Assignment For Published Patent Application

Cisco Technology, Inc. a California Corporation;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 10/02/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Reviewed By:	Date: 10/27/0
No Action Required:	
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and

Delivering high-current power and ground voltages using top side of chip package substrate

Preliminary Class

361

LICENSE FOR FOREIGN FILING UNDER Title 35, United States Code, Section 184 Title 37, Code of Federal Regulations, 5.11 & 5.15

GRANTED

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UNITED STATES PATENT APPLICATION

FOR

DELIVERING HIGH-CURRENT POWER AND GROUND VOLTAGES USING TOP SIDE OF CHIP PACKAGE SUBSTRATE

INVENTORS:

Sergio Camerlo, a citizen of the United States of America Yida Zou, a citizen of China Luca Cafiero, a citizen of Italy Gary L. Myers, a citizen of the United States of America Bobby Parizi, a citizen of the United States of America Hsing-Sheng Liang, a citizen of Taiwan

ASSIGNED TO:

Cisco Technology, Inc. a California Corporation

PREPARED BY:

THELEN, REID, & PRIEST P.O. BOX 640640 SAN JOSE, CA 95164-0640 TELEPHONE: (408) 292-5800 FAX: (408) 287-8040

Attorney Docket Number: CISCO-6920

Client Docket Number: 6920

Docket No. CISCO-6920

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Sergio Camerlo et al.

SERIAL NO.:

10/614,905

FILING DATE:

July 7, 2003

TITLE:

Delivering High-Current Power and Ground Voltages Using Top Side of Chip

Package Substrate

EXAMINER:

Unassigned

ART UNIT:

2836

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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

PATENT PRACTITIONERS TO BE MADE OF RECORD PURSUANT TO 37 C.F.R. § 1.32(c)(3)

Please recognize the following <u>ten</u> patent practitioners in the attached Declaration and Power of Attorney as being of record in the application or patent to which the power of attorney is directed:

David B. Ritchie, Reg. No. 31,562 Marc S. Hanish, Reg. No. 42,626 John P. Schaub, Reg. No. 42,125 Thierry K. Lo, Reg. No. 49,097 Masako Ando, (37 C.F.R.§10.9 (b)) Robert E. Krebs, Reg. No. 25,885 Khaled Shami, Reg. No. 38,745 Steven J. Robbins, Reg. No. 40,299 Hal J. Bohner, Reg. No. 27,856 Adrienne Yeung, Reg. No. 44,000 The Commissioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No. 50-1698.

Respectfully submitted, THELEN REID & PRIEST LLP

Dated: November 4, 2004

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DECLARATION & POWER OF ATTORNEY

a below-named inventor, I hereby declare that:

The specification of this subject matter:

My correct city and state of residence, my post office address and my citizenship are stated below next to my name.

I believe myself to be the original, first and sole inventor (if only one name is listed below) or an original and first joint inventor (if more than one name is listed below) of the subject matter which is disclosed and claimed and for which a patent is sought on the invention entitled:

"Delivering High-Current Power and Ground Voltages Using Top Side of Chip Package Substrate"

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	was filed on	;	•		
	was assigned se	erial No;			•
	which was amer	nded on;		•	
application, ir do not believe my invention there sale in the Ur has not been application in representative design patent lack application in I here	ncluding the claims, e that the claimed ir thereof, or patented reof or more than or nited States of Ame patented or made to any country foreignes or assigns more trapplication) prior to accordance with 3 leby claim foreign pressure that accordance with 3 leby claim foreign pressure trapplication of the country transcription of the country transcription of the claim foreign pressure accordance with 3 leby claim foreign pressure acco	o disclose information which is mate	the United Stion in any countries the same wapplication, e issued becan application an application app	above. To States of A ountry bef as not in p and that if fore the da fon filed by on) or six xamination	America before fore my bublic use or on the invention ate of this y me or my legal months (for a an of this ation(s) for ication for patent
	EIGN APPLICATIO				Priority Claimed
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Number	Country	Month/Day/Year Filed	Yes	No	•
Number	Country	Month/Day/Year Filed	Yes	No	<u>. </u>
Number	Country	Month/Day/Year Filed	Yes	No	



PROVISIONAL PATENT APPLICATION(S)

I hereby claim the ber listed below:	nefit under 35 U.S.C. §119(e) of any U	Inited States provisional application(s)
Application Number	Filing Date	
Application Number	Filing Date	

PARENT PATENT APPLICATION(S)

I hereby claim the benefit under 35 U.S.C. §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in these prior United States application(s) in the manner provided by 35 U.S.C. §112, I acknowledge the duty to disclose material information as defined in 37 C.F.R. §1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application.

Application No.	Filing Date	Status (Issued, Pending, Abandoned)
Application No.	Filing Date	Status (Issued, Pending, Abandoned)
Application No.	Filing Date	Status (Issued, Pending, Abandoned)
Application No.	Filing Date	Status (Issued, Pending, Abandoned)

I hereby appoint David B. Ritchie, Registration No. 31,562; Robert E. Krebs, Registration No. 25,885; Marc S. Hanish, Registration No. 42,626; John P. Schaub, Registration No. 42,125; Adrienne Yeung, Registration No. 44,000; Steven J. Robbins, Registration No. 40,299; Thierry K. Lo, Registration No. 49,097; William Samuel Niece, Registration No.: 47,824; J. Davis Gilmer, Registration No. 44,711; William E. Winters, Registration No. 42,232, Masako Ando, (37 C.F.R.§10.9 (b)); and John Klaas Uilkema, Registration No. 20,282; Becky L. Troutman, Registration No. 36,703; Hal J. Bohner, Registration No. 27,856; as attorneys of record with full power of substitution and revocation, to prosecute this application and transact all business in the United States Patent and Trademark Office connected therewith. If this application is assigned by me I agree and understand that the above-named attorneys will represent the assignee and not me.

Please send all correspondence and direct all telephone calls to:

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I, the undersigned, declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing therefrom.

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Doc. No. CISCO-6920

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FULL NAME OF	FIRST Name	MIDDLE Initial(s)	LAST Name		
INVENTOR 6	Hsing-Sheng		Liang	· 	
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	1431 Saratoga Avenue, #116	San Jose	California 95	129	
I further declare that all statements made herein of my own knowledge are true and that all statements made upon information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity					
of the application	or any patent issuing thereon.		7	+	
delole 6/30/2003 / de 6/30/2003					
Signature of Inventor 1 Date Signature of Inventor 2 Date					
Wico Signature of Inve	Pate Date	Signature of Inve	Myer 1/1/0		
Signature of Inve	7/1/0	3 Asyl-S Signature of Inve	ha Ley 7/1	<u>/@3</u>	



37 C.F.R. §1.56 Duty to disclose information material to patentability

(a) A patent by its very nature is affected with a public interest. The public interest is best served, and the most effective patent examination occurs when, at the time an application is being examined, the Office is aware of and evaluates the teachings of all information material to patentability. Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the Office which includes a duty to disclose to the Office all information known to that individual to be material to patentability as defined in this section. The duty to disclose information exists with respect to each pending claim until the claim is cancelled or withdrawn from consideration, or the application becomes abandoned. Information material to the patentability of a claim that is cancelled or withdrawn from consideration need not be submitted if the information is not material to the patentability of any claim remaining under consideration in the application. There is no duty to submit information which is not material to the patentability of any existing claim. The duty to disclose all information known to be material to patentability is deemed to be satisfied if all information known to be material to patentability of any claim issued in a patent was cited by the Office or submitted to the Office in the manner prescribed by §§1,97(b)-(d) and 1.98. However, no patent will be granted on an application in connection with which fraud on the Office was practiced or attempted or the duty of disclosure was violated through bad faith or intentional misconduct. The Office encourages applicants to carefully examine:

(1) Prior art cited in search reports of a foreign patent office in a counterpart application, and

(2) The closest information over which individuals associated with the filing or prosecution of a patent application believe any pending claim patentably defines, to make sure that any material information contained therein is disclosed to the Office.

- (b) Under this section, information is material to patentability when it is not cumulative to information already of record or being made of record in the application, and
 - (1) It establishes, by itself or in combination with other information, a prima facie case of unpatentability of a claim; or

(2) It refutes, or is inconsistent with, a position the applicant takes in:

- (i) Opposing an argument of unpatentability relied on by the Office, or
- (ii) Asserting an argument of patentability.

A prima facie case of unpatentability is established when the information compels a conclusion that a claim is unpatentable under the preponderance of evidence, burden-of-proof standard, giving each term in the claim its broadest reasonable construction consistent with the specification, and before any consideration is given to evidence which may be submitted in an attempt to establish a contrary conclusion of patentability.

(c) Individuals associated with the filing or prosecution of a patent application within the meaning of this section are:

(1) Each inventor named in the application;

(2) Each attorney or agent who prepares or prosecutes the application; and

(3) Every other person who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

(d) Individuals other than the attorney, agent or inventor may comply with this section by disclosing information to the attorney, agent, or inventor.